

## CURRICULUM VITAE

### 1. PERSONAL INFORMATION

Name : Dr.H.MANGALAM



### 2. EDUCATIONAL QUALIFICATIONS

Degree	College & University	Year of passing/Class obtained	Major subject with specialization
Ph.D	PSG College of Technology, Anna University Chennai	June 2009	VLSI Design and Testing
M.E	PSG College of Technology, Bharathiar University, Coimbatore	Feb 1993/ I 80.6%	Applied Electronics
B.E	PSG College of Technology, Bharathiar University, Coimbatore	May 1988/ I 72%	Electronics & Communication Engineering

**Title of Thesis at Doctoral Level:** Investigations on Power Minimization in Certain CMOS Digital VLSI Circuits

### 3. EMPLOYMENT EXPERIENCE

A) **Total Experience** : 27 years 7 months

B) **Teaching Experience** : 27 years 7 months

College & University	Designation	Year		Duration
		From	To	
Sri Krishna College of Engineering and Technology, Coimbatore	Professor and Head	1.6.2012	Till date	4Y 11M
	Professor	6.4.2011	31.5.2012	
Coimbatore Institute of Engg. And Technology, Coimbatore	Professor and Head	5.5.2010	31.3.2011	0Y 11M
Sri Shakthi Institute of Engineering and Technology, Coimbatore	Professor and Head	08.05.2008	30.4.2010	2Y 0M

Sri Krishna College of Engineering and Technology, Coimbatore	Associate Professor	01.09.2006	07.05.2008	1Y 8M
Sri Krishna College of Engineering and Technology, Coimbatore	Assistant Professor	19.12.2001	31.08.2006	4Y 8M
KOLEJ TAFE, Seremban, Malaysia	Lecturer	13.07.2001	14.12.2001	0Y 5M
Amrita Institute of Technology & Science, Coimbatore	Senior Lecturer	15.05.1999	30.06.2001	2Y 1M
Kumaraguru College of Technology, Coimbatore	Senior Lecturer	01.02.1995	14.05.1999	4Y 3M
Kumaraguru College of Technology, Coimbatore	Lecturer	10.06.1991	31.01.1995	3Y 8M
RVS College of arts and Science, Coimbatore	Lecturer	02.07.1990	08.06.1991	0Y 11M
Kumaraguru College of Technology, Coimbatore	Assistant Lecturer	01.07.1988	24.05.1990	2Y 1M

**C) Industrial Experience : NIL**

**D) Research Experience : i) One candidate obtained Ph.D degree in January 2016**

ii) 11 candidates are pursuing Ph.D under Part time. Amongst them, 5 are in the stage of synopsis submission

**E) Administrative Experience : 6Y 8M (As Head of Department)**

#### 4. PUBLICATIONS

##### A) International Journals

1. Gokila D. and **H.Mangalam**, "Programmable truncated multiplier", Journal of Computational and Theoretical Nanoscience, USA (in press) **(Scopus indexed)**
2. Kavitha K. and **H.Mangalam**, " Multilevel Spatial Modulation" Journal of the Chinese Institute of Engineers" (in press) **(Scopus indexed)**
3. N.K.Anushkannan and **H.Mangalam**,"Design of a CMOS PFD-CP module for a PLL", **Sadhana**, Vol.40, Part 4, June 2015, pp.1105-1116, Indian Academy of Sciences, ISSN 0256-2499, eISSN 0973-7677, **impact factor 0.587. (Scopus indexed)**
4. K.Kavitha and **H.Mangalam**, "Low complex decoding algorithm for multilevel space time trellis codes over MIMO channel" Int. Journal of Information and Communication Technology, Vol.8, No.1, pp. 69-78, 2016, **ISSN 1466-6642, eISSN 1741-8070 (Scopus Indexed)**

5. Rajasekar P. and **Dr.H.Mangalam**, "Design and Implementation of Low Power Multistage AES S Box", International Journal of Applied Engineering Research, Vol.10, No.19 (2015), pp.40535-40540, **ISSN 0973-4562, eISSN 1087-1090 (Scopus Indexed)**
6. K.Kalaiselvi and **H.Mangalam**, " FPGA Implementation and Performance Analysis of Stream Ciphers for Cryptographic Applications", International Journal of Applied Engineering Research, Vol.10, No.14 (2015), pp.34647-34651, **ISSN 0973-4562, eISSN 1087-1090 (Scopus Indexed)**
7. K.Kalaiselvi and **H.Mangalam**, "Power Efficient and high performance VLSI architecture for AES algorithm", Journal of Electrical Systems and Information Technology, Sep 2015, pp.178-183, **ISSN 2314-7172 (Scopus Indexed)**
8. Yamini Shanmugam and **H.Mangalam**, "Review and Performance Evaluation of Multiplexers using different logic styles for sub-micron technologies", International Journal of Applied Engineering Research, Vol.10, No.3 (2015), pp.7261-7274, **ISSN 0973-4562 eISSN 1087-1090 (Scopus Indexed)**
9. Yamini Shanmugam and **H.Mangalam**, "Analysis of the effect of inverter of different sizes on the XOR/XNOR gates at reduced CMOS process technology", International Journal of Applied Engineering Research, Vol.10, No.23 (2015), **ISSN 0973-4562, eISSN 1087-1090 (Scopus Indexed)**
10. S.Sureshkumar and **Dr.H.Mangalam**, "Removal of different types of artifacts and impulse noise in gray scale images", Journal of Theoretical and Applied Information Technology, vol.69, no.2, pp.305-317, November 2014, **ISSN :1992-8645 and E-ISSN:1817-3195 (Scopus Indexed)**
11. Janeera K. and **Dr.H.Mangalam**, "A new fast access low power multilevel cache architecture", International Journal of Innovative Research in Computer and Communication Engineering, Vol.2, No.7, pp.5025-5031, July 2014. **ISSN: 2320-9798, E-ISSN: 2320-9801.**
12. K.Kalaiselvi, **H.Mangalam** and K.Manjunathan, "Design of Low Power High Speed Adders using Modified Self-Restting Logic", **Middle-East Journal of Scientific Research, 22 (6), pp.829-834, 2014, ISSN 1990-9233, EISSN : 1999-8147**
13. K.Kalaiselvi, **H.Mangalam** and K.Manjunathan, "Design of Area Optimized High Speed Adder Circuits in Self Resetting Logic Style", **IOSR Journal of VLSI and Signal Processing, Vol.4, Issue.2, Ver.3, pp.31-48, Mar-Apr 2014, e-ISSN:2319-4200, p-ISSN:2319-4197.**
14. Kavitha K. and **H.Mangalam**, " Multilevel Spatial Multiplexing – a space time trellis coded modulation system for fast fading MIMO channel", International Journal of Engineering and Technology, 6(1), pp.273-277, Feb-Mar 2014, **ISSN 0975-4024, (Scopus Indexed)**
15. Gokila D. and **H.Mangalam**, " Implementation of Configurable Floating Point Multiplier", Journal of theoretical and Applied Information Technology, vol. 64, no.2,

- pp.331-338, June 2014. **ISSN :1992-8645 and E-ISSN:1817-3195 (Scopus Indexed).**
16. Chandra Sekar .P, **Dr.H.Mangalam** and Malathy .S, “Load balanced reliable power aware multicast routing protocol for MANETs” Pensee Journal, Vol 76, No. 1, Jan 2014 **ISSN No. 0031-4773, impact factor 0.06, (Scopus Indexed).**
  17. R.Sivakumar and **H.Mangalam** “Real Time Based Ranging & Detection of an Adaptive Cruise Control in Automobiles”, International Journal of Research in Electronics & Communication Engg, IASTER 2013, Vol 1, Issue 2 Oct - Dec 2013, **ISSN No. 2348 – 0017.**
  18. **Dr.H.Mangalam**, N.K Anush Kannan, et.al “Design of Various PFD and Charge-Pump Architectures for a PLL- a Survey” CiiT International Journal of Digital Signal Processing, **Print: ISSN 0974 – 9705 & Online: ISSN 0974 – 9594**, Aug 2013.
  19. Prabhu. E, **Dr.Mangalam.H**, S.Karthick, “ A low power multiplier using encoding and bypassing technique” Journal of theoretical and Applied Information Technology, vol. 57, no.2, Nov 2013,ISSN :1992-8645 and E-ISSN:1817-3195,**(Scopus Indexed)**
  20. R.Sivakumar and **H.Mangalam**, “Radar based Vehicle Collision Avoidance System used in Four Wheeler Automobile Segments”, International Journal of Scientific and Engineering Research, Vol. 5, no.1, Jan 2014. **ISSN: 2229-5518, Impact Factor: 3.2, Google Scholar Indexed**
  21. K.Kalaiselvi and **H.Mangalam**, “Area Efficient High Speed and Low Power MAC Unit”, International Journal of Computer Applications, vol.67, no.23, pp. 40- 44, April 2013. E-ISSN: **0975 – 8887, EBSCO Indexed**
  22. E.Prabhu, **H.Mangalam** and K.Saranya , **Dr.H.Mangalam**, “Design of Low power Digital FIR filter based on bypassing Multiplier”, International Journal of Computer Applications, Vol.70, No.9, pp:5-8, May 2013. **E-ISSN: 0975 – 8887, EBSCO Indexed**
  23. S.Sureshkumar and **H.Mangalam**, “Wavelet-based Image Compression of Quasi Encrypted Grayscale images”, International journal of Computer applications, Vol. 45, No.12, pp.35-39, May 2012. **E-ISSN: 0975 – 8887, EBSCO Indexed.**
  24. R.Padmashini, K.Kavitha, **Dr. H. Mangalam**, “Performance Study of BICM- OFDM System Over AWGN and Slow Fading Channel”, International Journal of Advance in Communication Engineering, Vol.3, No.1, January-June 2011, pp-37-41. **ISSN: 0975-6094.**
  25. **Mangalam H.** and Gunavathi K. (2006), ‘Flipflops for Low Power and High Speed Performance’, International AMSE Journal of Modeling-A, Vol. 79, No. 4, pp. 75-86. **(Scopus Indexed)**
  26. **Mangalam H.** and Gunavathi K. (2006), ‘Gate and Subthreshold Leakage Reduced SRAM cells’, International ICGST Journal of Digital Signal Processing (DSP), Vol. 6, No. 1, pp. 51-55. ISSN: ACSE ISSN: 1687-4811 **(SCOPUS Indexed)**
  27. **Mangalam H.** and Gunavathi K. (2007), ‘Domino Logic circuit with Reduced Leakage and Improved Noise Margin’, International Journal of Applied Engineering Research (IJAER), Vol. 2, No.4, pp. 585-593. **ISSN 0973-4562 (SCOPUS Indexed), E- ISSN 1087--1090**

## B) International Conferences

1. Mangalam H., Gunavathi K. and Subramanian S. (2004), 'Flipflops for low power and high speed performance', Proceedings of International AMSE Conference on Modeling and Simulation, Lyon, France, pp. 5.13-5.16.
2. Mangalam H. and Gunavathi K. (2009), 'Low Power Domino Logic Carry generator', Proceedings of International Conference on Sensors, Security, Software and Intelligent Systems (ISSIS 2009), Coimbatore Institute of Technology, Coimbatore.
3. Mangalam H. and Gunavathi K. (2009), 'A Low Power Circuitry for Testing Data Retention Faults in SRAM Cell', Proceedings of International Conference on Systemics, Cybernetics and Informatics (ICSCI 2009), Hyderabad, India, Vol.1 pp.60-63.
4. R.Padmashini, K.Kavitha, Dr. H. Mangalam, "Performance Study of BICM- OFDM System Over AWGN and Slow Fading Channel", International Conference on Communication and Signal Processing, pp-131-136, March 2011.
5. R.Padmashini, K.Kavitha, Dr. H. Mangalam, "Performance Study of BICM-OFDM System over AWGN and Slow Fading Channel", in Proc. International Conference on Communication and Signal Processing (ICCS'11) at Karunya University, Coimbatore, 17-18 March 2011.
6. K.Kavitha, R.Padmashini, Dr.H.Mangalam, "Bit Interleaved Coded modulation Frequency Division Multiple Access System with a Concatenated Code", IEEE Explore ,3-5 June 2011, On page(s): 915 - 919 Print ISBN: 978-1-4577-0588-5. **(Scopus Indexed)**
7. K.Kavitha, C.Karthikha, Dr.H.Mangalam, "Performance Analysis of Space Time Trellis Code with Spatial Modulation over Rayleigh Fading Channel", The First International Conference on Recent Trends in Computational methods communication and Controls ICON3C'12 at Government College of Technology, Tirunelveli -7, April 4<sup>th</sup> 2012.
8. K.Kavitha, G.Santhakumar, Dr.H.Mangalam, "Two Element Multi Slot E-shape Patch Antenna Array Configurations for MIMO Communications", The First International conference on Recent Trends in Computational methods, Communication and Controls ICON3C'12 at Government College of Technology, Tirunelveli -7, April 4<sup>th</sup> 2012
9. Anushkannan N.K, Mangalam H. et al., " Comparison and analysis of various PFD architectures for a phase locked loop design", IEEE Xplore **(Scopus Indexed)**
10. K.Kavitha, G.Saranya, H.Mangalam, "Joint Source Channel Coding with Space Time Trellis Code Modulation" in IEEE International workshop on Women in Electromagnetics, Antenna Design, Wave Propagation and Applications (WIEM 2013), 1-2 March 2013 **(Scopus Indexed)**.
11. D.Gokila and H.Mangalam, "Design of Reconfigurable Radix-4 Floating Point Multiplier" in ICRAET 2013 on April 19<sup>th</sup> , 2013 organized by the ISTE, IEEE & IETE Professional Societies at SSM College of Engineering, Komarapalayam.
12. N.K Anushkannan and Dr. H.Mangalam, " Comparison and Analysis of Various PFD architectures for a Phase locked Loop Design", 2013 IEEE International Conference on Computational Intelligence and Computing Research, 26 - 28 Dec 2013 at Vickram College of Engineering, Madurai.
13. Gokul P R, Prabhu E, Dr.Mangalam H, "Performance Comparison of Multipliers based on Square and Multiply and Montgomery Algorithms," Proceedings of the

- IEEE International Conference on Green Computing, Communication and Electrical Engineering (ICGCCEE-2014), 6-8<sup>th</sup> March 2014, pp:465-469. **(Scopus Indexed)**
14. Remy M, Prabhu E, Mangalam H, "A Versatile Low Power Design of Bit-Serial Multiplier in Finite Fields GF (2<sup>m</sup>)," Proceedings of the IEEE International Conference on Communication and Signal Processing (ICCSP 2014), April 3-5 2014, pp:474-478.**(Scopus Indexed)**
  15. R.Sivakumar and H.Mangalam, "A Novel Approach of Doppler Shift Radar based Cruise Control System", IEEE International Conference on Green Computing, Communication and Electrical Engineering, March 2014 at Dr.NGP Institute of Technology, Coimbatore.
  16. Suriya.K, Mangalam.H, "Design of UWB Slot Antenna for Cognitive Radio Networks", in International Conference on "Electrical, Instrumentation and Communication Engineering – Recent Trends and Research Issues"-(ICE<sup>2</sup>-RTRI 2015) held at Sri Krishna College of Technology, Coimbatore, January 2015
  17. R.Sivakumar and H.Mangalam, "A Heuristic Approach on Implementation of Combined Active and Passive Safety Systems in Hatch back Cars", in International Conference on "Electrical, Instrumentation and Communication Engineering – Recent Trends and Research Issues"-(ICE<sup>2</sup>-RTRI 2015) held at Sri Krishna College of Technology, Coimbatore, January 2015.
  18. Chandrasekar, H.Mangalam, "Reliable power aware multicast routing protocol for MANET" in International Conference on "Electrical, Instrumentation and Communication Engineering – Recent Trends and Research Issues"-(ICE<sup>2</sup>-RTRI 2015) held at Sri Krishna College of Technology, Coimbatore, January 2015
  19. D.Gokila and Dr.H.Mangalam, "Implementation of Low Power Multi Precision Floating Point Multiplier" in International Conference on "Electrical, Instrumentation and Communication Engineering – Recent Trends and Research Issues"-(ICE<sup>2</sup>-RTRI 2015) held at Sri Krishna College of Technology, Coimbatore, January 2015.
  20. S.Sureshkumar and Dr.H.Mangalam, "Removal of Strip lines, Blotches with Impulse Noise in Images" in International conference on Electrical, Instrumentation & Communication Engineering Recent trends and research issues, ICE<sup>2</sup>-RTRI 2015, held at Sri Krishna College of Technology, January 2015.
  21. O.Vignesh and Dr.H.Mangalam, "FPGA Implementation for Image Retrieval using Wavelet Transform" in International Conference on "Electrical, Instrumentation and Communication Engineering – Recent Trends and Research Issues"-(ICE<sup>2</sup>-RTRI 2015) held at Sri Krishna College of Technology, Coimbatore, January 2015.

### **C) National Conferences**

1. K.Kalaiselvi, H.Mangalam and K.Manjunathan "Area optimization of high speed Adders with self resetting logic", National conference conducted by Department of ECE (NCCCC2014), Hindusthan College of Engineering and Technology, Coimbatore. April 10<sup>th</sup>, 2014.
2. K.Kalaiselvi, H.Mangalam and Dibesh "High performance constant delay logic style", National conference conducted by Department of ECE (NCCCC2014), Hindusthan College of Engineering and Technology, Coimbatore. April 10<sup>th</sup>, 2014.
3. Maheswari, Dr.H.Mangalam, N.K Anush Kannan, "Design of Quadrature LO for a PLL", National conference on Electrical Systems in Electrical & Electronics Engg, 6<sup>th</sup> April 2013, RVS College of Engineering & Technology, Coimbatore.
4. Maheswari, Dr.H.Mangalam, N.K Anush Kannan, "Design of LC-VCO &

- Architecture of Quadrature LO for a PLL”, National Conference on Recent trends in Engineering & Technology, 10th April 2013, KSR Institute of Engineering & Technology, Tiruchengode.
5. Dr.H.Mangalam, N.K Anush Kannan, et.al., “ Design of Various PFD & CP Architecture for a PLL- A Survey,” proceedings in DRDO Sponsored 2 Day National conference on Emerging trends in Data Mining, 3<sup>rd</sup> & 4<sup>th</sup> Sep 2013, Tamilnadu College of Engineering, Coimbatore.
  6. R.Padmashini, K.Kavitha, Dr.H.Mangalam, “Bit interleaved Coded Modulation – Orthogonal Frequency Division Multiplexing System with a Concatenated code”, Proc. National Conference on Recent Trends in Communication Computation and Signal Processing (RTCSP’11), 1-2 March 2011 at Amrita Vishwa Vidyapeetham University, Coimbatore.
  7. R.Padmashini, K.Kavitha, Dr. H. Mangalam, “BICM –OFDM over Wireless channels”, Proc. National Conference on Information and Network management (NCINM’11), 17-18 March 2011 at Thiagaraja College of Engineering, Madurai.
  8. R.Padmashini, K.Kavitha, Dr. H. Mangalam, “Bit interleaved coded modulation-orthogonal frequency division multiplexing system with a concatenated code”, third National Conference on Recent trends in Communication Computation and Signal Processing, pp-48-51, 2011.
  9. Mangalam H. and Gunavathi K. (2003), ‘A Novel Approach for Synthesis of FSM Targeting Power Reduction’, Proceedings of National Conference on VLSI Design and Test (NCVDAT), PSG College of Technology, Coimbatore.
  10. Mangalam H. and Gunavathi K. (2003), ‘Power Optimal Clock Distribution in VLSI Circuits’, Proceedings of IEEE VLSI Design and Test symposium (VDAT 2003), Indian Institute of Science, Bangalore, pp. 244-246.
  11. Mangalam H., Gunavathi K. and Subramanian S. (2004), ‘A Low Power Asynchronous Pipeline FIFO’, Proceedings of IEEE VLSI Design and Test symposium(VDAT 2004),Infosys Leadership Training Institute, Mysore, pp.207-211.
  12. Mangalam H., Gunavathi K., Subramanian S. and Prabhu G. (2005), ‘Domino Logic with Variable Body Biased Keeper’, Proceedings of IEEE VLSI Design and Test Symposium(VDAT 2005), WIPRO Learning Institute, Bangalore, pp. 397-400.
  13. Mangalam H. and Gunavathi K. (2006), ‘Power Optimal Clock Distribution in VLSI Circuits’, Proceedings of the National Conference on Emerging Trends in Instrumentation Systems (NCETIS’06),Sri Ramakrishna Engineering College, Coimbatore, pp. 159-163.
  14. Mangalam H., Gunavathi K. (2006), ‘Dual Threshold Domino Logic’, Proceedings of National Conference on Emerging trends in VLSI Design (NCVD’06), PSG College of Technology, Coimbatore.
  15. Mangalam H. and Gunavathi K. (2006), ‘A Novel Technique to Reduce Leakage Currents in SRAM’, Proceedings of National Conference on Emerging trends in VLSI Design (NCVD’06),PSG College of Technology, Coimbatore.
  16. Mangalam H., Nirmaladevi M. and G.Sundarraaj (2001), ‘EXPRO- an expert system for analysis of TCP/IP protocol’ National Conference on CAD, Amrita Institute of Technology and Science, Coimbatore.

## 5. GUEST LECTURES DELIVERED

S. No	Topic of Lecture	Programme	Date
1	Leakage Reduction Techniques in VLSI Design	AICTE- ISTE sponsored summer school at Government College of Technology, Coimbatore	21.05.08
2	Challenges in Low Power Design of VLSI Systems	ISTE student chapter of Kumaraguru College of Technology, Coimbatore	18.02.09
3	Introduction to CMOS VLSI Circuits	AICTE sponsored FDP on VLSI Design and Testing at Sri Shakthi Institute of Engg. & Technology, Coimbatore	12.04.10
4.	Leakage reduction techniques in CMOS Digital VLSI Circuits	AICTE sponsored FDP on VLSI Design and Testing at Sri Shakthi Institute of Engg. & Technology, Coimbatore	19.04.10
5.	Low Power Digital Signal processing	Workshop by IEEE Chapter Chennai at AU, Chennai	09.10.10
6.	Power Aware VLSI Design	National Conference on Communications, signal processing and VLSI design, Sri Ramakrishna Engineering College, Coimbatore	18.04.11
7.	Leakage Reduction Techniques in VLSI circuits	AICTE sponsored SDP on Advanced VLSI Design and its Applications, Karpagam College of Engineering, Coimbatore	12.05.11
8.	Research trends in Low power VLSI Design	One day workshop on Recent trend in VLSI Design, JCT College of Engineering and Technology, Coimbatore	01.10.12
9.	Leakage Reduction in Memories	One day Workshop on "SoC in Embedded Memory, PSG College of Technology, Coimbatore	14.12.13



10	Trends in VLSI design and its applications	Invited talk in Institution of Engineers(India) Coimbatore local centre, PSG college of Technology, Coimbatore	31.01.14
11	Transmission line parameters	Anna University sponsored FDP on Transmission lines and waveguides, SREC, Coimbatore	18.06.14
12	Information Theory and Coding	Anna University sponsored FDP on Communication Theory at Hindustan College of Engineering and Technology, Coimbatore	11.12.14
13	Leakage Reduction in Cache Memory	FDP on VLSI Design at Karpagam University, Coimbatore	06.06.15

## 6. WORKSHOPS /FDPS/SEMINARS ATTENDED

S. No	Name of the Programme	Venue	Duration/Dates	No. of days
1	Workshop on "Electro medical Instruments"	Kumaraguru College of Technology, Coimbatore	September 17-18 , 1999	2
2	Short Term Course on "Neural Networks and its Engineering Applications" – QIP	<b>IIT Kanpur</b>	June 26 to July 01, 2000	1 week (6 days)
3	Workshop on "Principles and Methods of Source Coding for Speech, Image and Video"	Organized by Centre for Industry-Institute Interaction, Vellore Engineering College, Vellore	February 16-17, 2001	2
4	Continuing Education Programme on "Virtual Instrumentation" including practice	Conducted by PSG-NI, PSG College of Technology, Coimbatore	May 13-17, 2002	1 week (5 days)
5	NCVDAT 2003 - national conference on emerging trends in VLSI design and testing	PSG College of Technology, Coimbatore	February 21-22, 2003	2
6	Seminar on "VLSI Back-End Design through simulation with exposure to Mentor Graphics &Tanner Pro"	Advanced Training Institute for Information Technology , Chennai	March 26,2003	1

7	Techno Vision 2003-national seminar cum exhibition	Organized by Computer India at Coimbatore	July 11-12, 2003	2
8	VDAT 2003 - 7 <sup>th</sup> VLSI Design and Test Workshops	<b>Organized by VLSI Society of India at IISC, Bangalore</b>	August 28-30, 2003	3
9	National Seminar on System on Chip	SASTRA Deemed University, Thanjavur	November 27-29, 2003	3
10	VLSI Design 2004 – the 17 <sup>th</sup> international conference on VLSI Design& 3 <sup>rd</sup> International Conference on Embedded Systems Design <b>(with fellowship)</b>	<b>Organized by VLSI Society of India at IIT Bombay</b>	January 5-9, 2004	5
11	VDAT 2004 - 8 <sup>th</sup> VLSI Design and Test Workshops <b>(with fellowship)</b>	<b>Organized by VLSI Society of India at Infosys , Mysore</b>	August 26-28, 2004	3
12	Workshop on “Recent Advances in Memory Technology, Design & Test”	<b>Organized by VLSI Society of India at Bangalore</b>	January 10-11, 2005	2
13	VDAT 2005 - 9 <sup>th</sup> IEEE VLSI Design and Test Workshops	<b>Organized by VLSI Society of India at Wipro, Bangalore</b>	August 10-13, 2005	4
14	VLSI Design 2006 – the 19 <sup>th</sup> International Conference on VLSI Design& 5 <sup>th</sup> international conference on Embedded Systems Design <b>(with fellowship)</b>	<b>Organized by VLSI Society of India at Taj Krishna, Hyderabad</b>	January 3-7, 2006	7
15	NCVD 2006 -Second National Conference on emerging trends in VLSI design	PSG College of Technology, Coimbatore	March 3-4, 2006	2
16	Workshop on “E-Course and Learning Management Systems”	<b>Organized by NPTEL, PSG College of Technology, Coimbatore</b>	December 15-16, 2006	2
17	VLSI Design 2008 – the 21 <sup>st</sup> International Conference on VLSI Design& 7 <sup>th</sup> international conference on Embedded Systems Design	<b>Organized by VLSI Society of India at HICC, Hyderabad</b>	January 4-8, 2008	5
18	National workshop on Opportunities on Govt. funded R&D projects for academic institutions	<b>Anna University, Coimbatore</b>	October 30-31, 2009	2
19	Leadership Development Programme	Organised by SKI management at Pollachi	December 11-12, 2016	2
20	Workshop on Goal setting	SKCET, Coimbatore	January 24, 2016	1

## 7. GRANTS RECEIVED

- i) A grant of **Rs.15000** from TNSSTC as sponsorship towards the conduct of Fourth National Conference on Communication technology Interventions for Rural and Social Development –NCCTIRD '13 on 5.2.15 at Sri Krishna College of Engineering and Technology, Coimbatore
- ii) A grant of **Rs.90000** from **CFD- Anna University Chennai** for the conduct of Faculty Development Programme on “Electromagnetic Fields” from 9.12.13 to 15.12.13 at Sri Krishna College of Engineering and Technology, Coimbatore.
- iii) A grant of **Rs.3.5 Lakhs** from **AICTE** for the conduct of Faculty development program on “Recent trends in design of antennas for millimeter waves” from 10.06.13 to 22.06.13 at Sri Krishna College of Engineering and Technology, Coimbatore
- iv) A grant of **Rs.35000** from **DRDO** as sponsorship towards the conduct of Second National Conference on Communication technology Interventions for Rural and Social Development –NCCTIRD '13 from 06.02.13 to 07.02.13 at Sri Krishna College of Engineering and Technology, Coimbatore
- v) A grant of **Rs.2.1Lakhs** from **AICTE** for the conduct of Faculty development programme on “VLSI Design and Testing” during April 2010 from 12.04.10 to 23.04.10 at Sri Shakthi Institute of Engineering and Technology, Coimbatore

## 8. AWARDS

- Produced one Ph.D under Anna University Chennai in the year 2016
- Received felicitation for the good administration in 2015
- Guided a student project submitted for the National level Motorola Scholarship Award 2007 which had won the First Prize of Rs. 1 Lakh cash and a certificate.
- Won the best paper award in the National conference on Emerging Trends in VLSI Design and Test (NCVDAT) held at PSG College of Technology, Coimbatore during March 2006.
- Won the Best Project Award in UG Project in 1988.
- Received the State level Merit Certificate in X standard in 1982.

## 9. PROJECT PROPOSALS SUBMITTED TO FUNDING AGENCIES

- A proposal on “Vehicle Collision Avoidance System in Hatch Back Car Segments” submitted to UGC under minor project proposal scheme.
- A proposal on “Wireless Sensor Networks based Alert System for Elephant intrusion” submitted to Ministry of Environment and Forests” .

## 10. MEMBERSHIP IN PROFESSIONAL BODIES

- Member of Institution of Engineers, India since 2000
- Life Member of Indian Society for Technical Education since 1998
- Life Member of Systems Society of India since 2006
- Member of VLSI society of India since 2004

## **11. SERVICE TO PROFESSION/PROFESSIONAL ACTIVITIES IN THE POST HELD**

- Administrator of the department
- Taught courses in UG /PG level and produced excellent results
- Had developed and updated various labs in the department
- NAAC and NBA Coordinator
- Attended meetings of professional bodies if convenient.
- Delivered guest lectures /invited talks when invited.
- Member of the Governing Body of the college during 2012-2014
- Chairman for Anti Sexual-Harassment Cell and Grievances Redressal Committee
- Chairman for Board of Studies of ECE - Curriculum development and Syllabus framing
- Chairman for Result passing Board, Chief Examiner for question paper setting and evaluation
- Anna University nominee for Board of Academic studies of Karpagam College of Engg, Coimbatore
- Acted as Chair/Co-Chair of various technical symposiums/conferences conducted by Engg. Colleges
- Organized Guest Lectures/Seminars/Workshops/ /symposium to enhance the technical knowledge/skills, organizational skills and leadership qualities of students
- Organized Faculty Development Programmes/Conferences to enrich teaching-learning process blended with research concepts
- Organized value added courses/training programmes with resource persons from industries to enhance the practical knowledge/skills
- Organized industry visits /internships to bridge the gap between academia and industry
- Student counselor and Project Coordinator
- Research supervisor

## **12. PROFESSIONAL MISSION**

- To provide quality engineering education by adopting conceptual based teaching methodologies and creating motivation to improve the technical skills and research attitude among students. Modern appropriate teaching aids would be used for a precise and understandable delivery of lectures
- To inculcate interest among students to carry out innovative research projects
- To contribute effectively in curriculum development and framing course syllabus to meet the technological growth
- To carry out research activities leading to good publications/ patents
- To promote Research and Development by carrying out funded projects / consultancy work
- To work sincerely with commitment, dedication and passion in consonance with institutional goals