

## FACULTY PROFILE

Name : T.JOBY TITUS  
 Designation : ASSISTANT PROFESSOR(Sr.Gr)  
 Email ID : jobytitus.ece@srit.org



Area of Specialization : Low power VLSI  
 Total Experience : Academic : 7 Year 9Months  
 Industry : NIL

### Educational Qualification:

Degree	Branch / Specialization	College	University
Ph.D(Pursuing)	Information and communication /Low power VLSI	Anna University, Chennai	Anna University, Chennai
M.E.	Applied Electronics	The Rajaas Engineering college, Vadakangulam. Tirunelveli Dist	Anna University, Chennai.
B.E.	Electrical and Electronics Engineering	St.Xavier's Catholic college of Engineering, Chunkankadai, K.K.District.	Manonmaniam Sundaranar University, Tirunelveli.

### Experience Details:

S.No	Institution/Industry	Design.	Experience		Total Years & Months
			From	To	
1	Sri Ramakrishna Institute of Technology, Coimbatore.	Assistant Professor (Sr.Gr)	01-10-2014	-	1 year 5 months
2	Sri Ramakrishna Institute of Technology,	Assistant Professor	01-08-2010	30-09-2014	4 years 2 months
3	Sri Ramakrishna Institute of Technology,	Lecturer	02-06-2008	30-07-2010	2 years 2months
Total (as on Feb 2016)					7 Years 9 Months

Patents Application No: 7115/CHE/2015, "A device for data transaction between electronic gadgets using visible light communication.

Publications :

Journals :

1. A paper titled "Power Estimation in 6T SRAM using Recovery Boosting" has Published in the International Journal of Innovations in Engineering and Technology (IJET), vol 2 Issue1-Feb2013.
2. A paper titled "Power Optimization Technique for VLSI circuits using Fine Grain clock controller at Leaf nodes" has Published in the Asian Journal of Information Technology (IJIT), vol 13 Issue2-Feb2014.
3. T.Joby Titus and Dr.V.Vijayakumari Published a paper titled "Grained Network for Leakage power Optimization in FPGA Controlled Devices" in the Journal of Advances in Natural & Applied Sciences, 8(19) Special 2014, Pages:129-137.
4. T.Joby Titus published a paper titled " Clock node power performance analysis in CMOS processor", in International Journal of Advanced Research Trends in Engineering and Technology, Vol. 2, Issue 11, November 2015.
5. T.Joby Titus published a paper titled "Performance Analysis and Characterization Of Clock Network In CMOS Processor" Journal of Applied Sciences Research,Vol 11(22),Nov2015,Pages:60-65

International Conferences:

1. Presented a paper titled " New current sensing techniques for low power SRAM" in the International Conference on " Computing Techniques, Embedded Systems & Drives" organized by the School of Electrical Sciences at PPG Institute of Technology during 7 – 9, March 2012.
2. Presented a paper titled "Leakage power minimization using LUT power gating controller" in International Conference on Electrical, Instrumentation and Communication Engineering – ICE2 – RTRI at Sri Krishna College of Technology, Coimbatore during 2 and 3, JAN 2015.

National Conferences:

1. Presented a paper titled "Dynamic Power Reduction in FPGA Control Devices using Leaf Node Clock Controller" in the second National Conference on "Emerging Trends in Electronics, Instrumentation, Automation and Control at Karpagam College of Engineering, Coimbatore during 15, March 2013.
2. Presented a paper titled "Clock Network Power Minimization using Dual VDD/Dual Frequency" in the First National Conference on "Communication, Signal Processing and IC Design (NCCSPID'14) at Adithya Institute of Technology, Coimbatore during 28 and 29, March 2014.
3. Presented a paper titled "LUT Based Dynamic Power Minimization in Clock Network" in the Sixth National Conference on "Signal Processing, Communication and VLSI Design (NCSCV'14) at Anna University Regional Centre, Coimbatore during 9 and 10, May 2014.

Seminar, Conference, Workshop Organized:

S.No.	Title	Duration	Level (Intern./ National)
1	National Conference on "Communication, Computation, Control and Automation (CCCA 2008)"	2 days	National
2	AICTE sponsored seminar on "Recent Trends in High Data Rate Wireless communication"	2 days	National
3	National Conference on "Communication, Computation, Control and Automation (CCCA 2010)"	2 days	National
4	Two days IETE sponsored seminar on "Modern Trends in Wired & Wireless Communication"	2 days	National
5	Two days National Seminar sponsored by Ministry of Earth Sciences on "Wireless Networks for Rural Applications"	2 days	National

6	Two days National Seminar sponsored by Ministry of Earth Sciences on “Design and Applications of VLSI Techniques”	2 days	National
7	International Workshop sponsored by IEEE Electron Device Chapter Madras on “Nano Electronics”	1 day	International
8	National Conference on “Communication, Computation, Control and Automation (CCCA 2010)”	2 days	National
9	National seminar on “Digital Image Processing and its Applications”	1 day	National
10	ISTE Workshop on “Analog Electronics”	Two week	National
11	National workshop on “Advanced Trends in Communication Techniques”	1 day	National
12	ISTE Workshop on “Signals and systems”	Two week	National

Seminar, Conference, Workshop Attended:

1. Participated in the AICTE sponsored two days seminar on “Recent Trends in High Data Rate Wireless Communication” conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 20 – 21, August 2009.
2. Participated in the IETE sponsored two days Seminar on “Modern Trends in Wired & Wireless Communication” conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 29 – 30, December 2010.
3. Participated in the two days National Seminar sponsored by Ministry of Earth Sciences on “Wireless Networks for Rural Applications” conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 10- 11, October 2011.
4. Participated in the Workshop on “Need Based Approach for Effective Teaching” Organized by Sri Ramakrishna Institute of Technology, Coimbatore on 21-06-2011.

5. Participated in the two days National Seminar sponsored by Ministry of Earth Sciences on “Design and Applications of VLSI Techniques” conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 12 – 13, October 2011.
6. Organized and Participated an International Workshop sponsored by IEEE Electron Device Chapter Madras on “Nano Electronics” conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology on 30/12/2011.
7. Participated in the two days National Seminar sponsored by TNSCST and DRDO on “Strategies in Enhancing Research” conducted by Centre for Research, Anna University of Technology, Coimbatore during 16-17, February 2012.
8. Participated in the two days ISTE Workshop on “Aakash for Education” conducted by Indian Institute of Technology Bombay under the National Mission on Education through ICT (MHRD) at Sri Ramakrishna Institute of Technology during 10-11, November 2012.
9. Participated in one day workshop on “Design of Nano-scale CMOS VLSI Circuits” conducted by the Department of Electronics and Communication Engineering in association with Virtual Technology, Coimbatore at Sri Krishna College of Engineering and Technology, Coimbatore during 22, January 2013.
10. Participated in one day National seminar on “Digital Image Processing and its Applications” conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 18, March 2013
11. Participated in two week ISTE Workshop on “Analog Electronics” conducted by Indian Institute of Technology Kharagpur under the National Mission on Education through ICT (MHRD) at Sri Ramakrishna Institute of Technology during 4-14, June 2013.
12. Participated in one day National workshop on “Advanced Trends in Communication Techniques” conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 8, October 2013.
13. Participated in two week ISTE Workshop on “Signals and systems” conducted by Indian Institute of Technology Kharagpur under the National Mission on Education through ICT (MHRD) at Sri Ramakrishna Institute of Technology during 2-12, January 2014.
14. Participated two-day Workshop e-yantra organized by IIT-Bombay at Dr. G. R. Damodaran College of Science, Coimbatore, on February 24 and 25, 2016.

Guest Lectures Delivered:

Topic	Institution	Date(s)
Linear Integrated circuit simulation in TINA	Sri Ramakrishna Institute of Technology	07-02-2015
Linear Integrated circuit simulation in TINA	Sri Ramakrishna Institute of Technology	27-02-2016

Contributions to the Institution, Department and Society:

1. Acted as Deputy Warden for Sri Ramakrishna Institute of Technology Boys' Hostel during June to December 2008.
2. Accompanied the final year B.E (ECE-B) students for an Industrial Visit to Bangalore during the year 2008-2009.
3. Accompanied the second year B.E (ECE-B) students for an Industrial Visit to PRICOL Ltd, Coimbatore during the year 2008-2009.
4. One of the Auditing Member for whole college furniture and hostel stocks at Sri Ramakrishna Institute of Technology during 2008.
5. Acted as Department Timetable in-charge during the year 2009-2010 and 2010-2011.
6. Acting as SRIT Central Stores In-charge since 2010.
7. Acted as University LAB Timetable in-charge during the year 2011-2012 and 2012-2013.
8. Accompanied the Third year B.E (ECE-A) students for an Outbound training to Aanaikatti during the year 2011-2012.
9. One of the Auditing Member for Department of Mechanical and Civil Engineering at Sri Ramakrishna Institute of Technology since 2012.
10. Anti ragging squad member for Sri Ramakrishna Institute of Technology since 2010.
11. Accompanied the second year B.E (ECE-A) students for an Industrial Visit to S&T Workshop, Podanur during the year 2013-2014.
12. One of the Committee members for College day and Graduation day at Sri Ramakrishna Institute of Technology since 2009.
13. Acted as stall member representing Sri Ramakrishna Institute of Technology, Coimbatore for confederation Indian Industry conclave held at coodissia during Nov 2013.

14. Acted as stall member representing Sri Ramakrishna Institute of Technology, Coimbatore for “The Hindu Educational Expo held at Suguna Kalayana Mandapam, coimbatore during 9-10 March 2014.
15. RRC Programme Officer for Sri Ramakrishna Institute of Technology from 2009 to till date.
16. Auditing Member for Department of Civil Engineering at Sri Ramakrishna Institute of Technology during 2015.

Awards and Recognitions:

1. Best paper award for paper presented titled “LUT Based Dynamic Power Minimization in Clock Network” in the Sixth National Conference on “Signal Processing, Communication and VLSI Design (NCSCV’14) at Anna University Regional Centre, Coimbatore during 9 and 10, May 2014.
2. Mentor for Texas Instruments funded project “vehicle gas leakage detection in car with intelligent rear window control system” 2015.