

FACULTY PROFILE

Name : Dr.A.N.Jayanthi
Designation : Associate Professor /ECE
Email ID : jayanthi.ece@srit.org
Area of Specialization : VLSI DESIGN, SIGNAL PROCESSING, NETWORKS
Total Experience :17 Academic :17
Industry : NIL



Educational Qualification:

Degree	Branch / Specialization	College	University
B.E.	ECE	Sri Ramakrishna Engineering College	Bharathiar university, Coimbatore
M.E.	VLSI Design	Government College Of Technology, Coimbatore	Anna University, Chennai
Ph.D.	VLSI Design	Sri Ramakrishna Engineering College	Anna University, Chennai

Experience Details:

S.No	Institution	Design.	Experience		Total Years & Months
			From	To	
1	Sri Ramakrishna Institute of Technology, Coimbatore	Associate Professor	1.10.2014	Till date	1 year 5 months
2	Sri Ramakrishna Institute of Technology, Coimbatore	Asst.Prof (Selection Grade)	1.04.2011	30.09.2014	3 ½ years
3	Sri Ramakrishna Institute of Technology, Coimbatore	Asst.Prof (Senior Grade)	01.08.2010	31.03.2011	8 months
4	Sri Ramakrishna Institute of Technology, Coimbatore	Senior Lecturer	01.02.2008	31.07.2010	2 ½ years
5	Sri Ramakrishna Institute of Technology, Coimbatore	Lecturer	16.06.2003	31.01.2008	4 years 7 months
6	Bannari Amman Institute of Technology, Sathyamangalam	Lecturer	13.11.1998	30.04.2003	4 years 5 months
Total (as on Feb 2016)					17 Years 02 Months

Publications :

Journals :13

1. Jayanthi, AN & Ravichandran, CS, 'Performance optimization of Carry Select Adders using Variable Latency design style', Journal of Scientific and Industrial research ,Impact factor: 0.505,Vol 73,May 2014,pp 290-293.
2. Jayanthi, AN & Ravichandran, CS 2012, 'Design of an Error Tolerant Adder', American Journal of Applied Sciences, vol.9, no.6, pp. 818-824, Impact factor 1.4.
3. Jayanthi, AN & Ravichandran, CS 2013, 'Delay Optimisation In High-Performance Carry-Select Adders', Australian Journal of Basics and Applied Sciences, vol.7, no.9, pp.206-210.
4. Jayanthi, AN & Ravichandran, CS, 'Performance Comparison of High Speed VLSI Adders', Journal of Engg and Applied Science, ISSN 11101903,Vol 60,No1,Feb 2013,pp 65-79.
5. Jayanthi AN & Ravichandran, CS 2011, 'Design of a High Speed BCD Adder', European Journal of Scientific Research, vol.60, no.3, pp.422-427, ISSN 1450-216X, Impact factor 0.713.
6. Jayanthi, AN & Ravichandran, CS 2011, 'Variable Input Delay CMOS Logic for D Latch Circuit', International Journal of Emerging Technologies in Science and Engineering, vol. 4, no. 2, pp.68– 72, ISSN 1923-9181,2011.
7. Jayanthi, AN & Ravichandran, CS 2011, 'An Energy Efficient Layered Decoding Architecture for LDPC Decoder', CiiT International Journal, DOI: DSP062011013, vol. 3, no.5, pp.250-254.
8. Jayanthi, AN & Ravichandran, CS 2012, 'Comparison of High Speed VLSI Adders', in Archives Des Sciences, vol. 65, no. 11, pp.280-288, ISSN 1661-464X.
9. Jayanthi, AN & Ravichandran, CS 2013, 'Minimization of Area in Carry Select Adder', CiiT international Journal of DSP, Issue; DOI: DSP062013005.
10. Jayanthi, AN & Ravichandran, CS 2013, 'Comparison of Performance of High Speed VLSI Adders',IEEE Xplore, July 3rd, International Conference on Current Trends in Engineering and Technology, ICCTET'13.
11. Jayanthi, AN, R Nivedha,C Vani 2015,' GALVANIC SKIN RESPONSE MEASUREMENT AND ANALYSIS', International Journal of Applied Engineering Research, vol. 10, no. 16, pp.12447-12452, ISSN 0973-4562,Research India Publications.
12. A N Jayanthi , D.Prabhu, B.Sibiram 2015,'Low power ECRL logic based parallel self-timed adder 'International Journal of Applied Engineering Research, vol. 10, no. 6, pp.12494-12501, ISSN 0973-4562, Research India Publications.

13. M Nithya, Dr A N Jayanthi, N Sunil Prasad 2015, 'Redundancy in Tunneling between different networks', International Journal of Digital Communication Networks (IJDCN) vol. 2, no. 4, April 2015.

International Conferences: 4

1. Proceedings of "INTERNATIONAL CONFERENCE ON RESOURCE UTILISATION AND INTELLIGENT SYSTEMS" under the title of "BER Performances of OFDM System using Subcarrier Exclusion Techniques" on 4.1.2006, 5.1.2006, 6.1.2006
2. Proceedings of "INTERNATIONAL CONFERENCE ON EMERGING TRENDS IN COMMUNICATION" at Maria College of Engg and Tech, Attoor under the title of "11 ns 32 bit Carry Look ahead Adder using Quad tree Algorithm" on 8.3.2012 and 9.3.2012.
3. Proceedings of "INTERNATIONAL CONFERENCE ON ELECTRICAL, ELECTRONICS and COMMUNICATION NETWORKS -2013" at Adhithya Institute Of Technology, Coimbatore on 22.2.2013 and 23.2.2013 on "Delay and Area Optimisation in Carry Select Adders".
4. Proceedings of "INTERNATIONAL CONFERENCE ON CURRENT TRENDS IN ENGINEERING and TECHNOLOGY 2013" at Akshaya College of Engg and Tech, Coimbatore under the title of "Comparison of performance of High Speed VLSI Adders" on 3.7.2013.

National Conferences: 9

1. "THIRD NATIONAL CONFERENCE ON SIGNALS, SYSTEMS AND SECURITY" under the title of "DESIGN OF ENERGY EFFICIENT FULL ADDER USING HYBRID CMOS" at BANNARI AMMAN INSTITUTE OF TECHNOLOGY, Sathy during 28 and 29, February 2008.
2. "NATIONAL CONFERENCE ON COMMUNICATION, COMPUTATION, CONTROL AND AUTOMATION" under the title of "DESIGN OF WAVEPIPELINED MULTIPLIERS" at SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY, Coimbatore during 25 and 26, April 2008.
3. "TWO DAY NATIONAL CONFERENCE ON EMERGING TRENDS IN COMPUTER COMMUNICATION AND INFORMATICS (ETCCI-2011)," under the title of "AN ENERGY EFFICIENT LAYERED DECODING ARCHITECTURE FOR LDPC DECODER" at TAMILNADU COLLEGE OF ENGINEERING, COIMBATORE during MARCH 10-11, 2011.

4. Proceedings of "SECOND NATIONAL CONFERENCE ON EMERGING TRENDS IN ELECTRONICS ,INSTRUMENTATION, AUTOMATION and CONTROL ETEIAC'13 at Karpagam College of Engineering , Coimbatore on 15.3.2013 on "Delay minimization in 128 bit Carry Select Adders".
5. Proceedings of "SECOND NATIONAL CONFERENCE ON EXPANDING HORIZON IN COMPUTER,IT , TELECOMMUNICATION , ELECTRICAL and ELECTRONICS , EXCITE'13 at Prathyusha Institute of Technology and Management (PITAM) , Chennai on 2.4.2013 and 3.4.2013 on "Minimization of area in Carry Select Adder".
6. Proceedings of "NATIONAL CONFERENCE ON SCIENCE, RESEARCH and INFORMATION TECHNOLOGY NCSRIT'15" at SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY", Coimbatore on 14.3.2015 titled "GALVANIC SKIN RESPONSE MEASUREMENT AND ANALYSIS"
7. Proceedings of National Conference on Recent Advances in Computing, Communication and Electrical Systems RACCES'15 on 18th March 2015 titled "GALVANIC SKIN RESPONSE MEASUREMENT AND ANALYSIS" by A N Jayanthi,R Nivedha,C Vani.
8. Proceedings of National Conference on Recent Advances in Computing, Communication and Electrical Systems RACCES'15 on 18th March 2015 titled "LOW POWER ECRL LOGIC BASED PARALLEL SELF TIMED ADDER" by A N Jayanthi,D Prabhu,B Sibiram.
9. Proceedings of National Conference on 27th March 2015 titled "DESIGN OF PARALLEL SELF TIMED ADDER" by A N Jayanthi,Rajkumar , Rajsegar .

Seminar, Conference, Workshop Organized:

S.No.	Title	Duration	Level (Intern./ National)
1	DESIGN AND APPLICATIONS OF VLSI TECHNIQUES	12 .10.2011 13.10.2011	National

Seminar, Conference, Workshop Attended: 32

- 1.1Two day Workshop on “Networking , DSP and Biomedical Engineering” on 6.1.2003 and 7.1.2003 at Department of ECE,GCT Campus, AnnaUniversity.
- 2.Workshop on “RESEARCH APPLICATIONS USING MATLAB AND LABVIEW on 3rd and 4th April 2009 at Dr.Mahalingham College of Engineering and Technology, Pollachi.
- 3.Workshop on “MICROCONTROLLER BASED EMBEDDED SYSTEMS” under TEQIP on 13th and 14th of July 2007 at GCT, Coimbatore.
- 4.Workshop on “WIRELESS COMMUNICATION AND NETWORKS” on 9th October 2004 at Kumaraguru College of Technology, Coimbatore.
- 5.Workshop on “EMERGING TRENDS IN WIRELESS COMMUNICATION” on 29th January 2005 at Dr.Mahalingham College of Engineering and Technology, Pollachi.
- 6.Seminar on “RECENT TRENDS IN HIGH DATA RATE WIRELESS COMMUNICATION” on 20th and 21st August 2009 at Sri Ramakrishna Institute Of Technology, Coimbatore.
- 7.One day Workshop on “TECHNICAL COMMUNICATION” on 13th June 2009 at SSK College of Engineering and Technology, Coimbatore.
- 8.Attended “EDUCATORS DAY 2009”, Annual Technical Forum for faculty and Research Scholars conducted by National Instruments at Chennai.
- 9.AICTE Sponsored Workshop on “ RESEARCH ISSUES IN MEDICAL IMAGING TECHNIQUES” organized by Department of IT, SSK College of Engg and Tech on 30.5.2011 to 3.6.2011

- 10.** One day Workshop on “NEED BASED APPROACH FOR EFFECTIVE TEACHING” on 21.6.2011 at SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY”, Coimbatore-10
- 11.** Two day Ministry Of Earth Sciences Sponsored National Seminar on “DESIGN AND APPLICATIONS OF VLSI TECHNIQUES” conducted by the department of ECE, SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY”, Coimbatore-10 on 12 and 13 of October 2011.
- 12.** Two day Ministry Of Earth Sciences Sponsored National Seminar on “WIRELESS NETWORKS FOR RURAL APPLICATIONS” conducted by the department of ECE, SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY”, Coimbatore-10 on 10 and 11 of October 2011.
- 13.** One day International Workshop on NanoElectronics conducted by the department of ECE, SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY, Coimbatore-10 on 30.12.2011.
- 14.** One day workshop on “LATEX-A Tool for Research Articles “ conducted by the Department of Computer Applications , SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY”, Coimbatore-10 on 12.8.2011.
- 15.** Two day Workshop on “Aakash For Education” conducted by IIT Bombay on 3rd & 4th November 2012.
- 16.** Attended a LATEX Workshop offered by Spoken Tutorial Project , IIT Bombay, funded by National Mission on Education through ICT, MHRD, Government of India.
- 17.** Attended a LINUX Workshop offered by Spoken Tutorial Project , IIT Bombay, funded by National Mission on Education through ICT, MHRD, Government of India.
- 18.** Attended a PYTHON Workshop offered by Spoken Tutorial Project , IIT Bombay, funded by National Mission on Education through ICT, MHRD, Government of India.
- 19.** Attended a PHP and MySQL Workshop offered by Spoken Tutorial Project , IIT Bombay, funded by National Mission on Education through ICT, MHRD, Government of India.
- 20.** Two days National Seminar on “Research Issues in Wireless Communication” on 27.3.2013 and 28.3.2013 at Sri Ramakrishna Engineering College, Coimbatore.
- 21.** One day workshop on “Digital Image Processing and its Applications” on 18.3.2013 at Sri Ramakrishna Institute Of Technology, Coimbatore.

- 22.** Two days ISTE workshop on “Research Methods in Educational Technology” Conducted by IIT Bombay on 2nd and 9th February 2013.
- 23.** Two day Workshop on “Aakash For Education” conducted by IIT Bombay on 10rd & 11th November 2012.
- 24.** Two week ISTE workshop on “ Analog Electronics” conducted by IIT Kharagpur from 4th to 14th June 2013, held under National Mission on Education through ICT, MHRD, Government of India.
- 25.** Two days workshop on “Graphical System Design using LABVIEW “ organized by Department of ECE,EEE and Mechanical in Association with NI, Bengaluru on 19.2.2013 and 20.2.2013 at Sri Ramakrishna Institute Of Technology, Coimbatore.
- 26.** National workshop on “Advanced Trends in Communication Techniques” on 8.10.13 at Sri Ramakrishna Institute Of Technology, Coimbatore.
- 27.** Two week ISTE workshop on “ Signals and Systems ” conducted by IIT Kharagpur from 2nd to 12th January 2014, held under National Mission on Education through ICT, MHRD, Government of India.
- 28.** One day workshop on MathCAD on 23.1.2010 conducted by Department of EEE ,SSK College of engineering and Technology in Association with Paradigm Technologies.
- 29.** Two days workshop on “Strategies for effective Teaching on 10.06.2005 to 11.06.2005 at SRIT.
- 30.** Two week ISTE workshop on Control Systems conducted by IIT,Kharagpur under the National Mission on Education through ICT (MHRD,Government Of India) during 2nd to 12th December 2014 at RC 1249,Sri Ramakrishna Institute Of Technology,Cbe-10.
- 31.** AICTE Sponsored FDP on “Advanced Control Strategies and Applications” from 13.5.13 to 26.5.13 hosted by Department of EEE , Sri Ramakrishna Institute Of Technology, Coimbatore.
- 32.** Two days FDP on “Advanced Signal Processing Systems for Engineering Research” by Department of ECE, Sri Ramakrishna Engineering College ,Coimbatore-641022 during 19,20 March 2015.

Ph.D., Guidance / Supervisor ship:

Name of Scholar	Area of Research	Status
L MALATHI (Reg No: 1523469872)	Certain Investigations on Performance Enhancement of VLSI Multipliers	Course work
Mr S MUNAF (Reg No:1518469845)	Certain Methods for Performance Improvement in VLSI Architecture	Course work
Ms P Geetha (Reg No : 1224469789)	Secure Cryptosystems For Image Applications	Registration Confirmed
Mr P F Khaleelur Rahiman (Reg No : 11210531028)	A Case study of different noise reduction methods in Cochlear implant	Registration Confirmed
Mr Baskar D (RegNo:1314469259)	Certain Investigations On Digital Image Restoration Techniques	Registration Confirmed
Ms. Radhika P (Reg No : 1623269171)	Certain Investigations on performance parameters for a Multicasting environment in Communication Systems	Course work
Ms. Sugasini B (Reg No: 1624469442)	Certain Investigations on Biomedical Image processing Techniques and performance analysis	Course work
Ms. Leena B (Reg No:1624469413)	Certain Investigations on Deblurring and Denoising of Images using Image Processing	Course work

Guest Lectures Delivered:

Topic	Institution	Date(s)
Faculty Development Program on "MICROWAVE ENGINEERING"	TEJAA SAKTHI INSTITUTE OF TECHNOLOGY FOR WOMEN, Coimbatore	3.5.2011
Faculty Development Program on "VLSI DESIGN" in "Sequential Logic Design".	Sri Guru Institute of Technology, Coimbatore	16.12.2015

Membership in Professional Bodies:

- 1. Life Member in ISTE

Contributions to the Institution, Department and Society:

1. Rs 90,000 Grant Received for Seminar from MINISTRY OF EARTH SCIENCES MoES for conducting a 2 day National Seminar on “ Design and Applications of VLSI Techniques” on 12 and 13, Oct 2011.

2. Judge in CORNUCOPIA 2K11 on 16.3.2011 for Paper Presentation at National Level Technical Symposium at SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY”, Coimbatore conducted by ECE Department.

3. Acted as a Judge for Project Display FLASH OVER 2K11.

4. Internal Auditor for SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY

5. Acted as a Judge for Project Display- Engineer’s day Celebrations on 15.9.2014 by IETE.

6. Acted as a Judge for Project Display- Engineer’s day Celebrations on 15.9.2015 by IETE.

Women Empowerment Cell Member from January 2016

Resource person for a Faculty Development Program on “VLSI DESIGN” at Sri Guru Institute of Technology, Coimbatore on 16.12.2015 on “Sequential Logic Design”.

RESOURCE PERSON in Faculty Development Program on 23.5.2011 at Tejaa Sakthi Institute of Tech for MICROWAVE ENGINEERING

Microprocessor Laboratory incharge since June 2005.

One of the Accreditation incharge for the department of ECE

Senior Tutor for ECE B(2007-2011) Batch section

13. Senior Tutor for ECE A (2009-2013) Batch section

14. Stock Verifier for SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY for Physics, Chemistry, CSE. IT, MBA, MCA Departments till 2014.

15. Stock Verifier for SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY for Physics, Chemistry for 2015.

Awards and Recognitions:

1. SRIT Alumni Association awarded Dr S Radhakrishnan Award – ACADEMIC EXCELLANCE AWARD on 26.01.2015 at Sri Ramakrishna institute Of Technology.

2. Biography is available online in Marquis Who’s Who in 32nd Edition