

FACULTY PROFILE

Name : G. AYAPPAN
Designation : **ASSISTANT PROFESSOR
(SENIOR GRADE)**
Email ID : ayyappan.ece@srit.org



Area of Specialization : VLSI DESIGN

Total Experience : 8 years 7 month Academic : 8 years 7 month
Industry : Nil

Educational Qualification:

Degree	Branch / Specialization	College	University
B.E.	E C E	DR.MAHALINGAM COLLEGE OF ENGINEERING AND TECHNOLOGY	BHARATHIAR UNIVERSITY
M.E.	VLSI DESIGN	KONGU ENGINEERING COLLEGE	ANNA UNIVERSITY

Experience Details:

S.No	Institution/Industry	Design.	Experience		Total Years & Months
			From	To	
1	SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY	ASSISTANT PROFESSOR (SENIOR GRADE)	01.10.2014	29.02.2016	1 year & 5 months
2	SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY	ASSISTANT PROFESSOR	01.08.2010	30.09.2014	4 years & 2 months

3	SRI RAMAKRISHNA INSTITUTE OF TECHNOLOGY	LECTURER	03.12.2008	31.07.2010	1 year and 8 months
4	IFET COLLEGE OF ENGINEERING AND TECHNOLOGY	LECTURER	10.06.2008	24.10.2008	4 months
5	TAMILNADU COLLEGE OF ENGINEERING	LECTURER	20.06.2008	10.06.2008	1 year
Total (as on Feb 2016)					8 year and 7 months

Consultancy Work:

S. No.	Organization/ Agency	Nature of Work	Amount in Rs.	Year
1	Micromax Systems , Trichy	Circuit testing	2.0 Lakhs	1 year

International Conferences:

1. Presented a paper titled "**Implementation of Fast Adder Based on Quad Carry Look Ahead Adder**" in the IEEE International Conference on "**Computational Intelligence and Computing Research (IEEE ICCIC-2011)**" organized by the School of Electrical Sciences at Cape Institute of Technology, Kanyakumari during 15 – 18, December 2011.
2. Presented a paper titled "Dual carrier Modulation Technique using MATLAB for five level inverter" in the **International Conference on "Devices, Circuits and Systems (ICDCS-2012)"** organized by the Karunya University, Coimbatore during 15 – 16, March 2012(published in IEEE Digital library) .

Seminar, Conference, Workshop Attended:

1. Participated in the AICTE sponsored 2 days seminar on "**Recent Trends in High Data Rate Wireless Communication**" conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 20 – 21, August 2009.

2. Participated in the two days National Seminar on **“Network Simulator”** conducted by the Department of Computer Science and Engineering at PSG College of Technology, Coimbatore during 5 – 6, December 2009.
3. Participated in an Seminar on **“Intellectual Property Rights ”** conducted as a Value Added Course at Sri Ramakrishna Institute of Technology on 16/12/2009
4. Participated in the two days National Workshop on **“Signal and Image processing applications using Xilinx tools ”** conducted by the Department of Electronics and Communication Engineering at PSG College of Technology, Coimbatore during 9– 10, October 2010.
5. Participated in the IETE sponsored 2 days Seminar on **“Modern Trends in Wired & Wireless Communication”** conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 29 – 30, December 2010.
6. Participated in the 2 days National Seminar sponsored by Ministry of Earth Sciences on **“Wireless Networks for Rural Applications”** conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 10- 11, October 2011.
7. Participated in the 2 days National Seminar sponsored by Ministry of Earth Sciences on **“Design and Applications of VLSI Techniques”** conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 12 – 13, October 2011.
8. Participated in an International Workshop sponsored by IEEE Electron Device Chapter Madras on **“Nano Electronics”** conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology on 30/12/2011
9. Participated in Two day ISTE workshop on **“Aakash for Education”** conducted by Indian Institute of Technology Bombay at Sri Ramakrishna Institute of Technology during 10th & 11th November, 2012. This Workshop was held under the National Mission on Education through ICT(MHRD).
10. Participated in the 2 days Workshop sponsored by ENIXS Technology India Private Ltd. on **“Image Processing Framework using FPGA”** conducted by the Department of Electronics and Communication Engineering at Easwari Engineering College, Chennai during 6th & 7th , December 2012.
11. Participated in Two day ISTE workshop on **“Research Methods in Educational Technology”** conducted by Indian Institute of Technology Bombay at Sri Ramakrishna Institute of Technology

during 2nd & 9th, February 2013. This Workshop was held under the National Mission on Education through ICT (MHRD).

12. Participated in the one day National Seminar on **“Digital Image Processing and Its Applications”** conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 18 March, 2013.
13. Participated in the one day National Seminar on **“Advanced Trends in Communication Techniques”** conducted by the Department of Electronics and Communication Engineering at Sri Ramakrishna Institute of Technology during 8th October, 2013.
14. Participated in the Two week National workshop on **“Analog Electronics”** conducted by Indian Institute of Technology Kharagpur at Sri Ramakrishna Institute of Technology during 4th to 14th June, 2013. This Workshop was held under the National Mission on Education through ICT(MHRD).
15. Participated in the Two week National workshop on **“Signals and Systems”** conducted by Indian Institute of Technology Kharagpur at Sri Ramakrishna Institute of Technology during 2nd to 12th January, 2014. This Workshop was held under the National Mission on Education through ICT(MHRD).

Contributions to the Institution, Department and Society:

1. One of the stock verification member since 2011
2. Acting as member of Anti-Ragging squad since 2012
3. Time Table incharge during the academic year 2009-2010 and 2010-2011.
4. Electronic Circuit Laboratory Assist since June 2012.

Awards and Recognitions:

1. Mentor for Texas Instruments Innovation Challenge India Design Contest 2015 ,Project Titled **“Efficient Medical Supervision For Human Healthcare Monitoring System”**.