



Sri Ramakrishna Institute of Technology

(An Autonomous Institution)

(Accredited by NAAC with "A" Grade)

Pachapalayam, Perur Chettipalayam, Coimbatore – 641 010

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Faculty Profile

Full name with Qualification, Designation

Dr. P.S. Prakash

Professor and Head

Department of Computer Science and Engineering
Sri Ramakrishna Institute of Technology, Coimbatore



Date of birth	:	06.03.1971	Gender	:	Male
Nationality	:	Indian	Marital status	:	Married
Address for correspondence	:	46, Meena Estate, Sowripalayam Road, Coimbatore 641028	Permanent address	:	Same as Correspondence Address
Mobile no	:	9994525625	e-mail (official)	:	hod.cse@srit.org
Landline	:		e-mail (personal)	:	prakashpsrjn@gmail.com
Date of joining	:	01.06.2022	Post of joining	:	20.03.2023
No of promotions since D.O.J	:		Present post	:	Professor and Head
Present pay scale	:	37000-	Gross pay	:	88,386.00
Employee ID	:	TE428	Bank account No. (Salary credited)	:	548602010015978 Union Bank of India
P.F. No	:	TN/CBE/56764/10944	Group Insurance No	:	
PAN Number	:	ANFP0010M	Aadhar Number	:	
AICTE ID	:		AICTE Institution ID	:	

Educational Qualification

Degree	Specialization	Year of Passing	Name of the College	Name of the University	Class Obtained
UG	B.E EEE	1993	P.S.G.College of Technology, Coimbatore	Bharathiar	First
PG	M.E Electrical Machines	1996	P.S.G.College of Technology, Coimbatore	Bharathiar	First
	M.E CSE	2002	Kumaraguru College of Technology,Coimbatore	Bharathiar	First
Ph.D	Information and Communication Engg.	2011	P.S.G.College of Technology, Coimbatore	Anna university	NA
Others					

Registration for Ph.D. with area of interest:

Year of Ph.D. Registration : 2004

Year of Ph.D. Completed: 2011

Thesis Title: INVESTIGATIONS ON ADDITIVE MULTI-CONSTRAINED
QUALITY OF SERVICE ROUTING.

Supervisor Recognition Number: 2840062

Ph.D. Scholar Details

S.No.	Name of the scholar	Registration details	College	Status
1.	Preetha V	23244697421	SRIT,CBE	Course work
2.	Suryavarshini D	23254697264	SRIT,CBE	Course work
3.	Karthik V	18144991521	Chennai	Confirmed
4.	Julie Joan N S	18224991506	Chennai	Confirmed

Academic Experience

Name of the College	Designation	Joining Date	Relieving Date	Experience		
				Years	Months	Days
P.S.N.A college of Engineering and Technology, Dindigul	Lecturer	April 1998	Sep 1998		6	
Bannari Amman Institute of Technology, Sathyamangalam	Lecturer	Sep 1998	Feb 1999		6	
Sathyabma engineering College, Chennai	Lecturer	March 1999	May 2000	1	3	
Sri Ramakrishna Engineering College, Coimbatore	Lecturer	01.06.2000	2011	11		
RVS Technical Campus, Coimbatore	Dean	14.06.2012	May 2022	10		
Sri Ramakrishna Institute of Technology, Coimbatore	Professor	01.06.2022	--	1	4	
Total				24	7	

Industrial Experience

Name of the Organisation	Designation	Nature of Work	Joining Date	Relieving Date	Experience		
					Years	Months	Days
Vintech Devices and Control Coimbatore	Production Engineer	Design, Manufacturing and Testing of panel boards	1993	1998	5		
Total							

Courses Handled

Academic Year	Semester	Course Code	Course Name	Type of Course (T / L)	Pass Percentage
1998-2001			Electrical Machines DC Machines	Theory and lab	
			Electric Machines AC Machines	Theory and Lab	
			Basic Electrical Engineering	Theory and Lab	
			Electrical Drives and Control	Theory	
			Utilization of Electrical Energy	Theory	
2002-2023			Computer Networks	Theory	
			Networks Security	Theory	
			Mobile networks	Theory	
			Design of Computer Networks	Theory	
			Internet Security	Theory	
			Software Engineering	Theory	
			Computer Architecture	Theory	

Projects Guided

Academic Year	Semester	Project Title	UG / PG
		More than 50 Projects guided both UG and PG put together	

Publications

Publications	SCI / Scopus / Web of Sciences	Others
Journal	2	9
Conferences	1	3

INTERNATIONAL JOURNAL:

1. P.S.Prakash and S.Selvan, "Optimized Multi Constrained Quality of Service Routing Protocol" WSEAS Transactions on Information Science and Applications, Vol. 2, No. 8, pp. 80-95, 2011.
2. P.S.Prakash and S.Selvan, "Delay Coerced Multi Constrained Quality of Service Routing Algorithm" WSEAS Transactions on Information Science and Applications, Vol. 2, No. 8, pp.65-79, 2011.
3. P.S.Prakash and S.Selvan, "Optimized Multi Constrained Path Computation for QoS Routing" International Journal of Association for the advancement of Modeling and Simulation Techniques in Enterprises (A.M.S.E), Vol. 14, No. 1, pp. 21-40, 2009.
4. P.S.Prakash and S.Selvan, "Optimized Delay Constrained QoS Routing" International Journal of Association for the advancement of Modeling and Simulation Techniques in Enterprises (A.M.S.E), Vol. 14, No. 1, pp. 56-69, 2009.
5. P.S.Prakash and S.Selvan, "An Optimized and Efficient Approach for End-to-End Delay Constrained QoS Routing" International Journal of Association for the advancement of Modeling and Simulation Techniques in Enterprises (A.M.S.E), Vol. 14, No. 1, pp. 82-98, 2009.
6. P.S.Prakash and S.Selvan, "Coerced Delay and Multi Additive Constraints QoS Routing Schemes" International Journal of Computer Science and Engineering Vol. 3, No. 1, pp. 47-56, 2009.
7. P.S.Prakash and S.Selvan, "A Feasible Path Selection QoS Routing Algorithm with two Constraints in Packet Switched Networks" International Journal of Computer and Information Science and Engineering, Vol. 2, No. 2, pp. 129-135, 2008.
8. P.S.Prakash and S.Selvan, "A Characterized and Optimized Approach for End-to-End Delay Constrained QoS Routing" International Journal of Computer and Information Science and Engineering, vol. 2, No. 2, pp. 116-123, 2008.
9. P.S.Prakash and S.Selvan, "An Efficient and Optimized Multi Constrained Path Computation for Real Time Interactive Applications in Packet Switched Networks" International Journal of Computer Science, Vol.3 No.4, pp 293-300, 2008.
10. P.S.Prakash and S.Selvan, "Delay Specific Investigations on QoS Scheduling Schemes for Real- Time Traffic in Packet Switched Networks" International Journal of Information Technology, Vol. 4, No.2, pp.128-136,2008.

11. P.S.Prakash and S.Selvan “ An Overview of Quality of Service routing and Algorithms” International Journal of Association for the advancement of Modeling and Simulation Techniques in Enterprises (A.M.S.E), France.2009.

CONFERENCES/WORKSHOPS ORGANIZED:

1. Faculty Development Programme on “Network Management and Security” organized by Dept of Computer Science and Engineering Sri Ramakrishna Engineering College Coimbatore 641022, 2005
2. National level conference on “Recent Trends in Soft computing “ organized by Dept of Computer Science and Engineering (PG) at Sri Ramakrishna Engineering College Coimbatore 641022, 2006
3. One day Workshop on “Research Methodologies for Engineering Teachers” organized by Dept of Computer Science and Engineering (PG) at Sri Ramakrishna Engineering College Coimbatore 641022, 2006
4. AICTE Sponsored Staff Development Programme on “Research Directions in Grid Computing” organized by Dept of Computer Science and Engineering (PG) at Sri Ramakrishna Engineering College Coimbatore 641022, 2009

Conference Publications (IEEE format)

INTERNATONAL CONFERENCE:

1. P.S.Prakash, S.Selvan, “An Overview of QoS Routing and Algorithms” International Conference on Sensors, Security, Software and Intelligent Systems, Coimbatore Institute of Technology, (ISSSIS 2009) Coimbatore, India, pp. 22-37, Jan 2009.
2. P.S.Prakash, S.Selvan, “Optimized Delay Constrained QoS Routing”, in Proc. of World Academic of Science, Engineering and Technology, Singapore, Vol. 32, pp. 175-179, Aug 2008.
3. P.S.Prakash, S.Selvan, “Delay Analysis of Scheduling Algorithms for Real-Time Traffic in Packet Switched Networks”, in Proc. International Conference on Advanced Computing and Communications, ADCOM 2006, Mangalore, India, pp. 529-533, 2006.

Books / Book Chapters (IEEE format)

Sponsored Research

Project Title	Funding Agency	Amount Rs.	Duration

Consultancy

S. No.	Organization/ Agency	Nature of Work	Amount in Rs.	Year

Workshops / Seminars attended

Faculty Development Program attended (from recent)

Events

Conference

FDP

Resource person in “ICT tools for effective teaching-learning”, one day FDP organized by Sri Ramakrishna Polytechnic, Coimbatore on 16.12.2022.

Workshops

Served as Evaluator in “SRIT Hackathon 2023” organized by “Intelligent Signal Processing Research Cluster” held on 13.02.2023

Seminars

1. Seminar on “Effective communication Skills” at Sri Ramakrishna Institute of Technology, Coimbatore on 09. 01.2023

Awards and Recognitions Secured

Self-appraisal

Major Strengths

1. Team Player
2. Flexible
3. positive

Major Weakness

1. Discomfort with abundant and redundant paperwork
2. Adopting to monotonous work-routine.

Signature